

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
Hajime Kimura) Art Unit: 2652
Serial No.: 10/719,023)
Filed: November 24, 2003)
For: ANALOG CIRCUIT AND DISPLAY DEVICE AND)
ELECTRONIC DEVICE)

VERIFICATION OF TRANSLATION

Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

I, Kiyoshi Yanai, 101, Eideruhaimu, 1015, Aina, Atsugi-shi, Kanagawa-ken 243-0038 Japan, a translator, herewith declare:

that I am well acquainted with both the Japanese and English Languages;

that I am the translator of the English translation of the United States Patent Application Serial No. 10/719,023 filed on November 24, 2003; and

that to the best of my knowledge and belief the English translation is a true and correct translation of the United States Patent Application Serial No. 10/719,023 filed on November 24, 2003.

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date: this 21st day of May 2004



Name: Kiyoshi Yanai